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JC903 U.S. PTO
10/055910
10/28/02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Keshab K. Parhi

Appl. No. *(to be assigned)*

Filed: *(herewith)*

For: **Pipelining of Multiplexer
Loops in a Digital Circuit**

Confirmation No. *(to be assigned)*

Art Unit: *(to be assigned)*

Examiner: *(to be assigned)*

Atty. Docket: 1875.210000

Information Disclosure Statement

Commissioner for Patents
Washington, D.C. 20231

Sir:

Listed on accompanying Form PTO-1449 are documents that may be considered material to the examination of this application, in compliance with the duty of disclosure requirements of 37 C.F.R. §§ 1.56, 1.97 and 1.98.

Where the publication date of a listed document does not provide a month of publication, the year of publication of the listed document is sufficiently earlier than the effective U.S. filing date and any foreign priority date so that the month of publication is not in issue. Applicant has listed publication dates on the attached PTO-1449 based on information presently available to the undersigned. However, the listed publication dates should not be construed as an admission that the information was actually published on the date indicated.

Applicant reserves the right to establish the patentability of the claimed invention over any of the information provided herewith, and/or to prove that this information may not be prior art, and/or to prove that this information may not be enabling for the teachings purportedly offered.

This statement should not be construed as a representation that a search has been made, or that information more material to the examination of the present patent application does not exist. The Examiner is specifically requested not to rely solely on the material submitted herewith.

Applicant has checked the appropriate boxes below.

- ☒ 1. This Information Disclosure Statement is being filed within three months of the date of filing of a national application other than a continued prosecution application (CPA), OR within three months of the date of entry of the national stage as set forth in 37 C.F.R. § 1.491 in an international application, OR before the mailing date of a first Office Action on the merits OR before the mailing of a first Office Action after the filing of a request for continued examination under 37 C.F.R. § 1.114. No statement or fee is required.
- ☐ 2. This Information Disclosure Statement is being filed more than three months after the U.S. filing date AND after the mailing date of the first Office Action on the merits, but before the mailing date of a Final Rejection, or Notice of Allowance, or an action that otherwise closes prosecution in the application.
 - ☐ a. I hereby state that each item of information contained in this Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. § 1.97(e)(1).
 - ☐ b. I hereby state that no item of information in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application and, to my knowledge after making reasonable inquiry, was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. § 1.97(e)(2).

- ☐ c. Attached is our Check No. _____ in the amount of \$ _____ in payment of the fee under 37 C.F.R. § 1.17(p).
- ☐ 3. This Information Disclosure Statement is being filed more than three months after the U.S. filing date and after the mailing date of a Final Rejection or Notice of Allowance, but before payment of the Issue Fee. Enclosed find our Check No. _____ in the amount of \$ _____ in payment of the fee under 37 C.F.R. § 1.17(p); in addition:
 - ☐ a. I hereby state that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. § 1.97(e)(1).
 - ☐ b. I hereby state that no item of information in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application and, to my knowledge after making reasonable inquiry, was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. § 1.97(e)(2).
- ☐ 4. The document(s) was/were cited in a search report by a foreign patent office in a counterpart foreign application. Submission of an English language version of the search report that indicates the degree of relevance found by the foreign office is provided in satisfaction of the requirement for a concise explanation of relevance. 1138 OG 37, 38.
- ☐ 5. A concise explanation of the relevance of the non-English language document(s) appears below:
- ☐ 6. Copies of the documents were cited by or submitted to the Office in an IDS that complies with 37 C.F.R. § 1.98(a)-(c) in Application No. _____, filed _____, which is relied upon for an earlier filing date under 35 U.S.C. § 120. Thus, copies of these documents are not attached. 37 C.F.R. § 1.98(d).

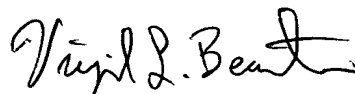
Applicant: Keshab K. Parhi
Appl. No. (to be assigned)

It is respectfully requested that the Examiner initial and return a copy of the enclosed PTO-1449, and indicate in the official file wrapper of this patent application that the documents have been considered.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.



Virgil L. Beaton
Attorney for Applicant
Registration No. 47,415

Date: 1/28/02

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FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT				ATTY. DOCKET NO. 1875.2100000		APPLICATION NO. (to be assigned)	
				APPLICANT Keshab K. Parhi			
				FILING DATE (herewith)		GROUP (to be assigned)	

U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA1						
	AB1						
	AC1						
	AD1						
	AE1						
	AF1						
	AG1						
	AH1						
	AI1						
	AJ1						
	AK1						

FOREIGN PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL1						Yes No
	AM1						Yes No
	AN1						Yes No
	AO1						Yes No
	AP1						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)			
	AR	1	Parhi, Keshab K. <i>et al.</i> , "Pipeline Interleaving and Parallelism in Recursive Digital Filter - Part I: Pipelining Using Scattered Look-Ahead and Decomposition," <i>IEEE Transaction on Acoustics, Speech, and Signal Processing</i> , Vol. 37, No. 7, July 1989, pp. 1099-1117.
	AS	1	Parhi, Keshab K. <i>et al.</i> , "Static Rate-Optimal Scheduling of Iterative Data-Flow Programs via Optimum Unfolding," <i>IEEE Transactions on Computers</i> , Vol. 40, No. 2, February 1991, pp. 178-195.
	AT	1	Keshab K. Parhi, "Pipelining in Algorithms with Quantizer Loops," <i>IEEE Transactions on Circuits and Systems</i> , Vol. 38, No. 7, July 1991, pp. 745-754.

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

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	APPLICANT Keshab K. Parhi	
	FILING DATE (herewith)	GROUP (to be assigned)

U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA2						
	AB2						
	AC2						
	AD2						
	AE2						
	AF2						
	AG2						
	AH2						
	AI2						
	AJ2						
	AK2						

FOREIGN PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL2						Yes No
	AM2						Yes No
	AN2						Yes No
	AO2						Yes No
	AP2						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)			
	AR	2	Keshab K. Parhi, "Low-Energy CSMT Carry Generators and Binary Adders," <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , Vol. 7, No. 4, December 1999, pp.450-462.
	AS	2	Kasturia, Sanjay <i>et al.</i> , "Techniques for High-Speed Implementation of Nonlinear Cancellation," <i>IEEE Journal on Selected Areas in Communications</i> , Vol. 9, No. 5, June 1991, pp. 711-717
	AT	2	

EXAMINER	DATE CONSIDERED
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